Job role	Digital Design Engineer
Department	HW SOC Design
Working place	Meyreuil - France

Mission

- Join the digital design team to take part in the development of the new architecture of Wisekey semiconductor's Secure ICs.
- Specification of microcontroller sub-systems & HW IPs.
- Design of microcontroller sub-systems in Verilog / System Verilog.
- Integration of HW IPs in microcontroller design.
- Design Verification (RTL and gate level simulations, code coverage, formal verification).
- Interface and support with other teams (analog design, memory design, back-End design, CAD…).
- Participate to IC silicon validation and debug.
- Follow internal quality design processes.

Main responsibilities

- Write design specification.
- RTL design, verification and integration of microcontroller subsystems.
- Develop simulation test bench and validation programs (assembly and/or C).
- Write design constraints.

Main interfaces

- HW Design team
- Firmware development team
- Project managers and Business Lines
- Product and test Engineer
- Security architecture team

Requirements

Educational background	Engineering Degree in microelectronics
Required experience	10 years in digital design practice
Required skills	Fluent RTL coding using Verilog
	Experience with microcontroller subsystems design
	Experience with assembler and C programming
	Experience with CAD flows
	Synthesis and Timing Analysis practices
	Be able to write design constraints for synthesis and STA
	Knowledge in common scripting languages (Python, csh, tcl, …)
	Knowledge in microcontroller interconnect protocols
	Knowledge in DFT
	Candidate must be creative, open-minded and appreciate teamwork
	Technical English