Job Role	**Device Engineer**
Department	R&D – Operations
Location / Working place	Meyreuil (France)

Missions

1) To manage Yield Improvement on our Products

2) To qualify new Foundry technologies and to support mask activities

3) To follow indicators (Yield, RMA, process changes, qualification, operation….)

Main responsibilities

1) To manage Yield Improvement on our Products

- To inform Design team about product weakness to be improved on new products
- To perform Gap analysis and set up action plan (Process, Device, PE/TE, Design …)
- To support Quality – PE – Design versus Customer Claim – Technical issue
- To defined “specifications” for new toolings needed for Device engineering analysis

2) To qualify new Foundry technologies and to support mask activities

- To qualify new Foundry technology and To Handle Technology survey as Device Engineering expert in WISEKEY Semiconductors company
- To manage Foundry Technology Survey – Publication of technology Roadmap
- To support R&D team in the test structures – test vehicles definition
- To support Purchasing Department for Quotations and in the negotiation about the different legal contracts

-To be the Key advisor to influence directions and strategies in device engineering field.

-To guide R&D team on the Design for Manufacturability (DFM), Design for Reliability (DFR).

3) To follow indicators (Yield, RMA, process changes, qualification ….)

- To Define Yield Target (NDO) & Budget by technology – product.
- To analyze yields, ND0 & Yield loss. Publication & analysis of yield, saving & loss on a monthly basis
- To manage the process changes at the Foundries for product cost (Yield) and reliability improvement.

-To closely work with operation team to manage analysis on testers, probe cards, operating methods and contribute to new tool qualifications and production ramp up.

Main interfaces

Internally :

- All WISeKey Semiconductors departments

Externally :

- OSATs
- Presto
- Foundries
- IP Providers
- Design houses

Requirements

Educational background / diplomas	Engineering degree or PhD
Experience	Device engineering (3-5 years)
Skills	- Physics / Microelectronics knowledge - Semiconductor process - Failure analysis systems & method - Statistical analysis & associated tools - Quality / reliability
Others	- Communication with third parties - Language: Fluent in English